




DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT

(Affiliated to Visvesvaraya Technological University, Belagavi & Approved by AICTE, New Delhi)

Opp. Art of Living, Udayapura, Kanakapura Road, Bangalore- 560082

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Accredited NBA, NAAC with A+, New Delhi

Faculty Name	RAMAKRISHNA S	
Academic Designation	ASSOCIATE PROFESSOR	
Educational Qualification	BE, M-Tech (VLSI Design & Embedded Systems)	
Experience in Years	7 years (Teaching) 32 years (Industry Experience)	
Area of Interest	AI, ML, DL, CV, DATA SCIENCE, BLOCK CHAIN, IOT, CLOUD COMPUTING, CYBER SECURITY, BIG DATA	
Date of Birth	13-01-1957	
Email ID	sramkrish2121@gmail.com	

Educational Details

- M-Tech VLSI Design and Embedded Systems (VTU EXTENSION CENTER-2011)
- BE Electronics (BMS College of Engineering-1982).

Personal Details

- Date of Birth: 13/01/1957
- Hobbies: Reading
- Languages Known: English, Kannada and Telugu

Professional Experience

- Worked as **Senior Scientist/Chief Engineer** in **R&D, ITI Ltd** from **15-01-1983 to 31-01-2015 (32 years)**
- **System Design, Software Design** for various **R&D projects (Defense/BSNL/Railways)**

Publications

International Conferences:

1. Embedded SNMP Agent for STM16 Encryptor Appliance based on NIOS II Processor in International Conference on Communication, Computation, Control and Nano Technology OCT29-30, 2010 at Rural Engineering College, BHALKI, BIDAR Dt.
2. Presented a detailed technical solution on Battlefield Management System (BMS) design, architecture, security and management for Indian Army Conference at New Delhi in 2014-15.

International Conferences:

3. Presented a Call Center design and development in SUPERCOMM International Conference/Exhibition at New Delhi in year 2002.

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National Conferences:

- Presented a paper "AI Technologies and its impact in Social life" in national Conference at DSATM 2019

Seminars Attended

- Network Simulations using NS2 and NS3 (Vemana Institute)
- ARM Cortex M3 Embedded controllers (DSATM)

Workshop Attended

FDPs attended on various technologies (5 Academic Years)
Artificial Intelligence-MLT,DL using LabView
Embedded System Designs, IoT and IoT security protocols
Recent Trends in science for Versatile engineering Applications (RSVEA-2019)
Network Simulations using NS2 and NS3 (Vemana Institute)
ARM Cortex M3 Embedded controllers (DSATM)
OBE Practices (DSATM)

Awards

1. Promoted as Senior Scientist for Core R&D formation by a panel of IISc professors and ITI Directors.
2. Appreciation Award received on successful completion of Call Center and Info-kiosk projects during 2002-03
3. Teacher Award on getting 100% result in three subjects IOT, WC & 4G Broadband, Fiber Optic Networks in the year 2019
4. Appreciation letter from Principal for teaching Python for EC students-2 batches

Academic Activity

- Member of a Project Review panel for analysis of final project presentations in ECE of DSATM
- NBA
- NAAC

Subjects taught:

Python Application Programming (3 Batches)
C,C++ OOPs (1 Batch)
Programming in C++ (1 Batch)
OS (2 Batches)
IoT and Wireless Sensor Networks (5 Batches)
LTE4G Cellular and Broadband (3 Batches)
Network Security (2 Batches)
Fiber Optic Networks (FON)-1 Batch
Computer Networks (CN) and CCN- (5 Batches)
OFC(3 Batches)
Digital Switching Systems (2 Batches)
Digital System Design Using Verilog (DSDV)-(1 Batch)
Management and Entrepreneur Development (1Batch)

VTU Work assigned:

WLTE - 2017-18(Even)
IoT & WSN paper setting 2017-18 (Odd)
FON Paper setting 2018-19 (Even)
IoT & WSN paper setting 2020-21(ODD)
Python 2020-21(EVEN)

Faculty Development Activity/Workshops

- FDPs attended on various technologies (5 Academic Years)
- Artificial Intelligence-MLT,DL using LabView
- Embedded System Designs, IoT and IoT security protocols
- Recent Trends in science for Versatile engineering Applications (RSVEA-2019)
- ARM Cortex M3 Embedded controller s (DSATM)
- OBE Practices (DSATM)

Contact Details

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