




DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT

(Affiliated to Visvesvaraya Technological University, Belagavi & Approved by AICTE, New Delhi)

Opp. Art of Living, Udayapura, Kanakapura Road, Bangalore- 560082

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Accredited NBA, NAAC with A+, New Delhi

Faculty Name	Dr.MALLIKARJUN P Y	
Academic Designation	Professor	
Educational Qualification	BE, M. Tech, Ph.D, LMISTE	
Experience in Years	24	
Area of Interest	VLSI & EMBEDDED SYSTEMS	
Date of Birth	01-04-1974	
Email ID	mallikarjunpy@gmail.com	

Educational Details

- Ph.D - in VLSI Design under VTU 2019.
- M.Tech in VLSI & Embedded Systems at VTU Regional Centre, Bangalore under VTU Belgaum 2010.
- B.E in STJ Institute of Technology, Ranebennur under Karnataka University, Dharwad 1997.

Personal Details

- Date of Birth: 01-04-974
- Hobbies: Cricket, Music, Nature
- Languages Known: English & Kannada

Professional Experience

Administrative Experience: 11 Years

- Worked as Dean of Academics and Professor & Head of the Department, Department of Electronics and communication Engineering, Karavali Institute of Technology, Mangalore- since August 2019 to March 2022.
- Worked as Head of the Department, Department of Electronics and communication Engineering, Don Bosco Institute of Technology, Bangalore-560074 since 2017 to Aug 2019.
- Worked as Head of the Department (ECE) at Sri Krishna Institute of Technology, Bangalore from 29-07-2010 to 09-02-2017.

Teaching & Industry Experience: 13 Years

- Worked as Senior Lecturer in the Dept of ECE, Don Bosco Institute of Technology, Bangalore from 11-08-2008 to 12-06-2010.
- Worked as Senior Design Engineer in Global Leader, Bangalore from 28-01-2003 to 05-07-2008.
- Worked as Lecturer in the Dept of ECE in MVJ College of Engineering, Bangalore from 08-01-1997 to 22-01-2003.

Publications

International Journals:

- “STDN based New Architecture for Power Network in 3D IC’s” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (An ISO 3297: 2007 Certified Organization) Vol. 2, Issue 11, November 2013.
- “Analyzing 3D PDNs Using Multiple Clock Domains to Obtain Worst-Case Power Supply Noise and Temperature Variations” International Journal of Scientific & Engineering Research, Volume 4, Issue 12, December-2013 1393 ISSN 2229-5518(IJSER).
- “PDN Optimization of 3DIC’s Using TSV Technology” International Journal of Advances in Science Engineering and Technology, ISSN: 2321-9009 Volume- 1, Issue-3, Jan.-2014(IJSET).
- "MMM algorithm for TSV based Three-Dimensional Integrated Circuits to obtain Low power and Reliable Clock" Published in Elsevier Editorial journal with Ms. Ref. No.: MEE-D-13-00436 on 24th August 2013.
- “Analyzing 3D PDNs Using Multiple Clock Domains to Obtain Worst-Case Power Supply Noise and Temperature Variations” International Journal of Scientific & Engineering Research, Volume 4, Issue 12, December-2013 1393 ISSN 2229-5518(IJSER).
- “Design of an optimum channel sensing mechanisms for Cognitive Radios” in “Test Engineering and Management ” ISSN:0193-4120, Volume-83, May-June 2020, Page No:4252-4256. (Scopus Indexed)
- Published a paper with the title “Design of bandwidth-based channel sensing mechanism for Cognitive Radio Networks” in “Pal arch’s Journal of Archaeology of Egypt/ Egyptology” ISSN: 1567-214x, Volume-17, September-2020, Page No. 4809-4825.
- Published a paper with the title “ Factor influencing Economic Growth of Online Food Ordering and Delivery Application During Covid-19 “ in International Journal of Aquatic Science, ISSN :2008-8019, Vol 12, Issue 01, 2021, Page No. 242-249.

International Conferences:

- "Mathematical and Statistical Analysis and Power Delivery Design for 3D IC’s using TSV Technologies” .The Second International Conference on “Emerging Research in Computing, Information, Communication and Applications”, ERCICA-2014:01-02, August, 2014 at NMIT ,Bangalore. Elsevier science.
- "MMM algorithm for TSV based Three-Dimensional Integrated Circuits to obtain Low power and Reliable Clock". International conference ICrtSIV at Don Bosco Institute of technology Bangalore on Feb 22nd 2014.
- “PDN Optimization of 3DIC’s Using TSV Technology” at international conference ICEECE held at Chennai on 1-12-2013.
- “Analysis of Power Transfer Modeling for Three Dimension Silicon Chip’s using ThSV Technologies”, IEEE-International Conference on Systems, Computation, Automation and Networking (ICSCAN 2018), organized by Manakula Vinayagar Institute of Technology on 6th and 7th July 2018, Pages: 1-4, Year:2018, DOI: 10.1109/ICSCAN.2018.8541177, Publisher: IEEE.
- “ThSV based Three Dimension Device to Optimize Low Energy and Reliable Timer using MNM Technique, 2018 3rd IEEE International Conference on Recent Trends in Electronics, Information and Communication Technologies (RTEICT-2018), Organized by SVCE, Bangalore on 18-19 May 2018, IEEE: 978-1-5386-2440-1.

- Presented a paper with the title “Design of an optimum channel sensing mechanisms for Cognitive Radios” in the second international conference on “Advances in Computing And Information Technology (IACIT-2020)” held during 29-30 April 2020, organized by school of Computing and Information Technology, REVA university, Bengaluru.

National Conferences:

- “Analyzing Power Distribution Network in TSV-Based 3-D Processor-Memory Stacks” National Conference on Communication and Image Processing – 2013 (NCCIP-13) held at T.JOHN Institute of Technology, Bangalore-83 on 7th August 2013.
- “Low power TSV based 3-d IC data path units” National Conference on Trends in VLSI Design and Technology (NCTVDT-2013) held at Don Bosco Institute of Technology, Bangalore on 02-05-2013 and 03-05-2013.
- “Power delivery network In 3-d integrated circuits through efficient estimation of TSV” Two days National conference on Frontiers and advances in information science and technology (FAIST’13) held at RajaRajeswari College of Engineering. On 23rd and 24th may 2013.
- “Realtime Implementation of Speech Recognition System with Vector Quantization (VQ) on TMS320C6713DSK” at National Conference ETBMSP-11 held on 26th feb 2011 at RV College of Engineering Bangalore.
- “SDR Baseband Signal processing using Model Design Approach” at Conference RCCIT11-Second National Conference on 8th April 2011 MS Engineering college Bangalore.

Conference

- Worked as Chief coordinator of National conference held at DBIT(2009)
- Worked as Convenor of National conference in the year 2014 at SKIT
- Worked as Convenor of National conference in the year 2016 at SKIT

Seminars

- Worked as chief coordinator of one day seminar on recent trends in communication engineering at DBIT in the year 2008
- Worked as chief coordinator of one day seminar on Cryptography at DBIT in the year 2009
- Worked as chief convener of one day seminar on 5G Technologies at SKIT in the year 2011
- Worked as chief convener of one day seminar on IOTs at SKIT in the year 2013

Workshop

- Workshop on IC design methodologies along with IETE at SKIT in the year 2014.
- Conducted workshop on IOTs along with IETE in the year 2015

Academic Activity

- Worked as Chief coordinator of NBA at Don Bosco Institute of Technology, Bangalore (2019)
- Worked as Chief coordinator of NAAC at Don Bosco Institute of Technology, Bangalore(2019)
- Officiated as chief Judge for Project competitions jointly organized by ICT along with Allianz, Bangalore in the month of March 2019.
- Participated BRIDGE -2018 BENGALURU -The largest Industry-Institute Interaction Event of Asia organized by I CT Academy on 26th of September 2018 at Bangalore.
- Worked as Chief convener of Distinction Award ceremony held at DBIT in the month of May2019.
- Worked as Chief convener of Alumni platinum meet season –II at DBIT in the month of Jan2019.
- Given guest lecture twice at Raja Rajeswari college of Engineering,Bangalore(2017)
- Worked as Chief convener of Blood donation camp at DBIT in the month of Nov2018.
- Officiated as subject expert for the staff selection at different institutions (VTU)

Faculty Development Activity/Workshops

- Officiated as chief Judge for Project Expo held at Jyothi Institute of Technology(2014)
- Worked as Chief coordinator of National conference held at DBIT(2009)
- Worked as Chief coordinator of cultural held at SKIT (2013)
- Worked as Chief coordinator of test in SKIT (2014)
- Worked as Nodal Officer for CBCG in SKIT(2015)
- Conducted workshop on IC design methodologies along with IETE.
- Organized one day National conference in the year 2014
- Conducted workshop on IOTs along with IETE in the year 2015
- Organized one day National conference in the year 2016

Contact Details

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