




DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT

(Affiliated to Visvesvaraya Technological University, Belagavi & Approved by AICTE, New Delhi)

Opp. Art of Living, Udayapura, Kanakapura Road, Bangalore- 560082

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Accredited NBA, NAAC with A+, New Delhi

Faculty Name	APOORVA RAGHUNANDAN	
Academic Designation	Assistant Professor	
Educational Qualification	BE, M. Tech	
Experience in Years	1	
Area of Interest	VLSI, Energy Harvesting	
Date of Birth	6th December, 1994	
Email ID	apoorva-ece@dsatm.edu.in	

Educational Details

- MTech in VLSI Design and Embedded Systems, RV College of Engineering, Bengaluru.
- B.E in Electronics and Communication Engineering, VTU.

Personal Details

- Date of Birth: 06/12/1994
- Hobbies: Indian Classical Music, Yoga, Poetry, Sanskrit
- Languages Known: Tamil, Kannada, English, Hindi, Sanskrit

Professional Experience

- October 2021 till date - Working as an Assistant Professor in Department of Electronics and Communication Engineering, Dayananda Sagar Academy of Technology and Management, Bengaluru
- June 2019 to July 2021 - Worked on a Research Project at RV College of Engineering, Bengaluru
- June 2020 to September 2021 - Worked as an Online tutor at teacheron tutoring platform
- Jan 2017 to April 2017 - Worked as a Project Trainee at Bharat Electronics Limited, Jalahalli, Bengaluru

Publications

International Journals:

- H. V. Ravish Aradhya, **Apoorva Raghunandan** "Performance Analysis of Advanced Adders under changing Technologies", International Journal of Innovations in Engineering and Technology (IJET), Vol 12, Issue 2, Jan 2019.
- Keerti Kulkarni, **Apoorva Raghunandan** "A Review on Mapping Land cover Change

and the Various Techniques used” International Journal of Science and Research (IJSR), Vol 6, Issue 3, March 2017, pp 1330-1339.

- Anuradha MR, **Apoorva Raghunandan**, “Smart Techniques Employed for Monitoring Agricultural Parameters” International Journal of Science and Research, (IJSR)” Vol 6, Issue 4, April 2017, pp 1832-1841.
- **Apoorva Raghunandan**, Anuradha MR” The Methods Used in Text to Braille Conversion and Vice Versa”. International Journal of Innovative Research in Computer and Communication Engineering, Vol 5, Issue 3, March 2017, pp 5297-5310.

International Conferences:

- **Apoorva Raghunandan**, Shilpa D R,” Design of High - Speed Hybrid Full Adders using FinFET 18nm technology”, 4th IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT 2019), May 2019.
- **Apoorva Raghunandan**, Shilpa D.R” Design of Electrostatic Energy Harvesting Pre charge circuit”, 4th IEEE International Conference on Communication and Electronics Systems (ICCES 2019).
- **Apoorva Raghunandan**, H. V. Ravish Aradhya, Mohana, Pakala Raghav, “Object Detection Algorithms for Video Surveillance Applications” 7th IEEE International Conference on Communication and Signal Processing, Melmaruvathur, April 2018.
- **Apoorva Raghunandan**, H. V. Ravish Aradhya “Area and Timing Analysis of Advanced Adders under changing Technologies”, 4th IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT 2019), May 2019.
- Shilpa DR, Rajith BK, **Apoorva Raghunandan** “Piezoelectric Energy Harvesting for Wearbles” 2nd IEEE International Conference on Communication, Computing & Industry 4.0(C2I4)-2021
- **Apoorva Raghunandan**, Shilpa DR “Design of FinFET Based Adiabatic Logic circuits in 18nm technology” 2nd IEEE International Conference on Communication, Computing & Industry 4.0(C2I4)-2021.

Conference Attended

- 4th IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT 2019), May 2019.
- 7th IEEE International Conference on Communication and Signal Processing, Melmaruvathur, April 2018
- 2nd IEEE International Conference on Communication, Computing & Industry 4.0(C2I4)-2021, CMRIT, Bengaluru.

Academic Activity

- NAAC Coordinator
- Internal Assessment Coordinator

Faculty Development Activity/Workshops

- Participated in a two-day workshop on “Network on Chip” at RV College of Engineering, Bangalore held on 18th and 19th Dec 2017.
- Participated in the ‘Conclave on Artificial Intelligence’ at IISC, Bangalore on the 16th of March 2018

- Participated in a three-day workshop on “STA, Analog Design and Universal Verification Methodology” at RV College of Engineering, Bangalore held on June 29th to July 1st, 2018.
- Participated in a workshop on “IPR and Patents” at R V College of Engineering in October 2017.

Contact Details

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