




**DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT**  
22 Mile, B.M Kaval, Udayapura, Kanakapura Road, Bangalore-560082.

**DEPARTMENT OF ELECTRICAL & ELECTRONICS**

Faculty Name	RAMYA S RAJAN	
Designation	Asst. Professor	
Educational Qualification	M.Tech	
Experience in Years	7.5 years	
Area of Interest	VLSI & Embedded Systems	
Date of Birth	20-05-1987	
Email ID	ramyasrajan-eee@dsatm.edu.in	

<b>Educational Details</b>
<ul style="list-style-type: none"><li>• M.Tech-VLSI and Embedded Systems</li><li>• B.E- Electrical and Electronics Engineering</li></ul>
<b>Personal Details</b>
<ul style="list-style-type: none"><li>• Date of Birth:20-05-1987</li><li>• Hobbies: Coin Collection, Reading books</li><li>• Languages Known: English, Kannada, Hindi, Tamil</li></ul>
<b>Professional Experience</b>
<ul style="list-style-type: none"><li>• Working as Asst Professor in DSATM at Bangalore from Aug 2013 to till date</li></ul>
<b>Publications</b>
<b>International Journals:</b>
<ul style="list-style-type: none"><li>• NIL</li></ul>
<b>National Journals:</b>
<ul style="list-style-type: none"><li>• NIL</li></ul>
<b>Conferences:</b>
<ul style="list-style-type: none"><li>• “Comparison of Multi Level Inverter using Reverse Voltage Topology for Multiple Loads”,<b>2019 1st International Conference on Advanced Technologies in Intelligent Control, Environment, Computing &amp; Communication Engineering (ICATIECE)</b> at Dayananda Sagar Academy of Technology &amp; Management, Bengaluru on 13<sup>th</sup> March 2019</li><li>• “A Novel Implementation of PFC Converter using PCCM Boost Technique”,<b>IEEE Conference on Recent Innovations in Electrical, Electronics &amp; Communication Engineering(ICRIEECE)</b> at Kalinga Institute of Industrial Technology, Bhubaneswar on 27<sup>th</sup>-28<sup>th</sup> July 2018</li><li>• “DFT Based Fault Detection Technique for Transmission Lines”, <b>First International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC) 2018</b> at Priyadarshini College of Engineering, Coimbatore on 28<sup>th</sup>&amp; 29<sup>th</sup> Jan 2018</li><li>• “Solar PV cell interfaced with Grid connected Multilevel Inverter for Distributed Power Generation”, <b>International Conference on Applied Science Engineering and Technology (ICASET-17)</b>, at Sri Sairam College of Engineering, Anekal, Bengaluru on 18<sup>th</sup> and 19<sup>th</sup> May 2017</li></ul>

- “Hybrid Converter with Simultaneous DC and AC Output”, *Third National Conference on Convergence of Science, Technology and Management(NCCSTM 2017)* at DSATM Bengaluru on 19<sup>th</sup> May 2017
- “Design and Simulation of Three Phase Multilevel Inverter fed Induction Motor Drive”, *Third National Conference on Convergence of Science, Technology and Management(NCCSTM 2017)* at DSATM on 19<sup>th</sup> May 2017
- “Soft Start of Single Phase Pump Motor”, *First National Conference on Green Computing Technologies(NGCT-2015)* DSATM on 6<sup>th</sup> May 2015
- “Determining Standard Cell Drive Strength Based on On-Chip Load Assessment”, *International Conference on VLSI, Communication, Advanced Devices, Signals & Systems and Networking (VCASAN-2013)* at BNMIT from 17<sup>th</sup>-19<sup>th</sup> July 2013
- “Accurate Power measurement methodology for VLSI circuits using CAD tools”, *International Conference on Devices, Circuits and Systems – ICDCS 2012* at Karunya University in collaboration with IEEE on 15<sup>th</sup> March 2012
- “Study of effectiveness of Circuit level Leakage Power Optimization Techniques in DSM CMOS cells”, *International Conference on Energy and Electrical Systems (ICEES 2011)*, Kuala Lumpur, Malaysia, August 12-14, 2011
- “Design of a 4 bit accumulator using new 45nm low leaky standard cells”, *IP-SoC 2011 Conference*, Grenoble, France, Dec 7-8, 2011

## Awards

- “Determining Standard Cell Drive Strength Based on On-Chip Load Assessment” Proceedings of International Conference on VLSI, Communication, Advanced Devices, Signals & Systems and Networking (VCASAN-2013), SPRINGER, BNMIT, Bangalore 17-19 July 2013, pp.117-123 was adjudged “the best research paper” on the occasion of International Conference on VLSI, Communication, Advanced Devices, Signals and Systems and Networking, VCASAN-2013

## Academic Activity

- Worked as NAAC Dept level Criteria 5 Coordinator , NBA Dept. Level Criteria 5 Coordinator, Time table Coordinator , Website Coordinator, Placement Coordinator, Test coordinator, Proctor, Class Teacher

## Faculty Development Activity

- Attended All India Council of Technical Education, Training and Learning Academy, online elementary 5 days Faculty Development Programme on "**Electric Vehicles and Charging Infrastructure**" at **National Power Training Institute - Power Systems Training Institute** from 26<sup>th</sup> -30<sup>th</sup> July 2021
- All India Council of Technical Education, Training and Learning Academy, online elementary 5 days Faculty Development Programme on "**Formal Verification of Digital Designs**" at **PES University** from 7<sup>th</sup> -11<sup>th</sup> June 2021
- All India Council Of Technical Education Training And Learning Online 5 Days Faculty Development Programme On "**Smart Cities**" at **DSATM** from 18<sup>th</sup> Jan to 22<sup>nd</sup> Jan 2021
- All India Council Of Technical Education Training And Learning Online 5 Days Faculty Development Programme On "**Lab On Chip**" At **Defence Institute of Advanced Technology (DU)** from 14<sup>th</sup> -18<sup>th</sup> Dec 2020
- All India Council Of Technical Education Training And Learning Online 5 Days Faculty Development Programme On " Artificial Intelligence" At **Jaypee Institute Of Information Technology** from 1<sup>st</sup> to 5<sup>th</sup> Oct 2020
- Online Faculty Development Program On "**Advances In Power Electronics Applications**" Organised By **Bhusanayana Mukundadas Sreenivasaiah Institute Of Technology And Management**, Bangalore from 4<sup>th</sup> -8<sup>th</sup> Aug 2020
- One Week Faculty Development Program – "**International Webinar On Artificial Intelligence Applications In Industrial Evolution 4.0**" Organised By **Bangalore Institute Of Technology**

from 13<sup>th</sup> to 17<sup>th</sup> July 2020

- Attended 5 day FDP on Future & Emerging Trends in Electrical and Electronics Systems with Artificial Intelligence (Machine Learning), IIOT & smart Grids using **LABVIEW from Jan-28<sup>th</sup> 2020 to Feb 01<sup>st</sup> 2020** in associations with **National Instruments by VI solutions** at Dayananda Sagar Academy of Technology and Management, Bangalore.
- Attended **AICTE** sponsored 5 day Workshop on **Cyber Security** organized by **NPTI from 2<sup>nd</sup> - 6<sup>th</sup> Dec 2019** at DSATM, Bangalore.
- Attended 5 day FDP on **Graphical Programming Data Acquisition and Hardware Programming** using **LABVIEW from june-28<sup>th</sup> 2019 to July 02<sup>nd</sup> 2019** in associations with **National Instruments by VI solutions** at Dayananda Sagar Academy of Technology and Management, Bangalore.
- Attended **Five Days Faculty Development Program on Bosch Rexroth PLC, Servo Drives, Sensors and SCADA on January 16<sup>th</sup> -20<sup>th</sup>, 2019**, in association with Bosch Rexroth & Institution of Engineers (INDIA) at Dayananda Sagar Academy of Technology and Management, Bangalore
- Participated in **Three Day workshop on NAAC assessment & accreditation from 31<sup>st</sup> Jan to 2<sup>nd</sup> Feb 2019** at Dayananda Sagar Academy of Technology and Management, Bangalore.
- Participated in **Five day training on LabVIEW MyRIO by National Instruments , 13<sup>th</sup> to 17<sup>th</sup> October 2018** at Dayananda Sagar Academy of Technology and Management, Bangalore.
- Attended **“Three day NBA workshop-2017”**, held on 11<sup>th</sup>,12<sup>th</sup>, and 13<sup>th</sup> December 2017 at Dayananda Sagar Academy of Technology and Management, Bangalore
- Attended in **Four days faculty development program on Latest advancements in power Engineering Sector, held on Jan 8<sup>th</sup> -11<sup>th</sup> , 2018** Dayananda Sagar Academy of Technology and Management, Bangalore
- Participated in fivedays workshop on **"NI LAB VIEW -Data Acquisition using myDAQ"** held on July 26<sup>th</sup> -31<sup>th</sup> , 2018 Dayananda Sagar Academy of Technology and Management, Bangalore
- Participated in threedays workshop on **"OBE PRACTICES"** held on Jun 18<sup>th</sup> -20<sup>th</sup> , 2018 Dayananda Sagar Academy of Technology and Management, Bangalore
- Participated in fivedays workshop on **"NI LAB VIEW CORE 1 & CORE 2 COURSE"** held on Jun 8<sup>th</sup> -13<sup>th</sup> , 2018 Dayananda Sagar Academy of Technology and Management, Bangalore

### **Student Project Funds Fetched with Grant No.**

- **KSCST Project Ref. No. 44S\_BE\_171 Titled “Hybrid Speed Controller for BLDC Motor” was granted Rs.7000 by KSCST : STUDENT PROJECT PROGRAMME – 44<sup>th</sup> SERIES**

### **Professional Membership**

- Nil

### **Contact Details**

Asst.Professor, Department of Electrical & Electronics Engineering  
Dayananda Sagar Academy of Technology & Management  
Udayapura, Kanakapura Road  
Bengaluru-560082  
Mail ID:ramyasrajan-eee@dsatm.edu.in

### **My Vision**

- To continuously strive to develop my knowledge, skills, and competence to better fulfill education, research, and service responsibilities of myself as well as my students